



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: )  
LENOBLE )  
Serial No. 10/714,440 )  
Filing Date: NOVEMBER 14, 2003 )  
For: PROCESS FOR FABRICATING A )  
SHORT-GATE-LENGTH MOS )  
TRANSISTOR AND INTEGRATED )  
CIRCUIT COMPRISING SUCH A )  
TRANSISTOR )

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SECOND PRELIMINARY AMENDMENT

Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Prior to the calculation of fees and examination of  
the present application, please enter the amendments and  
remarks set out below.